

## MR232-ISO technical manual

Single port Module for MCXBP-MR or UNXBP-MR  
MCXBP-MR is connected to MCXPCI/0 card  
UNXBP-MR is connected to UNXPCI/0 card.

### Technical characteristics

RS232 DTE interface with  $2K V_{\text{eff}}$  min isolation  
Signals supported : TxData, RxData, input and output control signal selected  
by dip switch  
Control output signal : RTS or DTR or TxCLOCK  
Control input signal : CTS or DCD or RI or DSR or RxCLK or TxCLK-I  
2 DIP8-switch SW1 and SW2  
Maximal data rate : 230 Kbps  
5 diagnostics LEDs  
145 mA max per MR232-ISO.  
ESD protection up to 15 KV  
EMI/RFI filtration  
Power protected by polyswitch

### LEDs :

POWER = DC power  
TX = Tx Data signal  
RX = Rx Data signal.  
CtrlOUT = Control output signal.  
CtrlIN = Control input signal

### Serial connector :

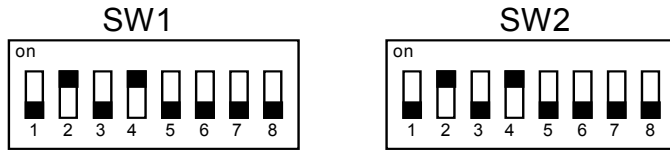
RS232 DTE interface.  
MALE SUBD 25 (UNX)  
FEMALE SUBD 25 (MCX)

### SW1 and SW2 settings

Output control signal definition	SW1	SW2
RTS	<p>on</p> <p>1 2 3 4 5 6 7 8</p>	<p>on</p> <p>1 2 3 4 5 6 7 8</p>
DTR	<p>on</p> <p>1 2 3 4 5 6 7 8</p>	<p>on</p> <p>1 2 3 4 5 6 7 8</p>
TXCLOCK (not available for UNX range board)	<p>on</p> <p>1 2 3 4 5 6 7 8</p>	<p>on</p> <p>1 2 3 4 5 6 7 8</p>

Input control signal definition	SW1	SW2
CTS	<p>on</p> <p>1 2 3 4 5 6 7 8</p>	<p>on</p> <p>1 2 3 4 5 6 7 8</p>
DCD	<p>on</p> <p>1 2 3 4 5 6 7 8</p>	<p>on</p> <p>1 2 3 4 5 6 7 8</p>
RI	<p>on</p> <p>1 2 3 4 5 6 7 8</p>	<p>on</p> <p>1 2 3 4 5 6 7 8</p>
DSR (not available for MCX range board)	<p>on</p> <p>1 2 3 4 5 6 7 8</p>	<p>on</p> <p>1 2 3 4 5 6 7 8</p>
TXCLK-I (not available for UNX range board)	<p>on</p> <p>1 2 3 4 5 6 7 8</p>	<p>on</p> <p>1 2 3 4 5 6 7 8</p>
RXCLOCK (not available for UNX range board)	<p>on</p> <p>1 2 3 4 5 6 7 8</p>	<p>on</p> <p>1 2 3 4 5 6 7 8</p>

Examples :  
 Output control signal = RTS  
 Input control signal = CTS



### SUBD 25 pinout

PIN N°	Input/Output	Description
1	I	PGND
2	O	TXD
3	I	RXD
4(*)	O	RTS
5(*)	I	CTS
6(*)	I	DSR
7	I	GNDI
8(*)	I	DCD
9		NC
10		NC
11		NC
12		NC
13		NC
14		NC
15(*)	I	TXCLK-I
16		NC
17(*)	I	RXCLOCK
18		NC
19		NC
20(*)	O	DTR
21		NC
22(*)	I	RI
23		NC
24(*)	O	TXCLOCK
25	I	GNDI

(\*) See SW1/SW2 settings